Abstracts

A 1-10 GHz 0.18/spl mu/m-CMOS chipset for multi-mode wireless applications

M. Madihian, H. Fujii, H. Yoshida, H. Suzuki and T. Tamazaki. "A 1-10 GHz 0.18/spl mu/m-CMOS chipset for multi-mode wireless applications." 2001 MTT-S International Microwave Symposium Digest 01.3 (2001 Vol. III [MWSYM]): 1865-1868 vol.3.

Performance results for a 1-10 GHz chipset developed in a low-cost two-metal 0.18 /spl mu/m-CMOS technology are described. The developed chips include RF and IF amplifiers, down- and up-mixers, VCO, and receiver/transmitter circuits. Each chip's circuit parameters as well as transistors gate widths are optimized for maximum frequency of operation. The receiver and transmitter chips have a conversion gain higher than 25 dB over 2.4-5.8 GHz, and higher than 16 dB for any other frequency within 1-10 GHz.

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